

Team Name: sdmay24-40

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Report Period: Nov 5-Nov19

### **Summary of Progress in this Period**

**ADC board is done and ordered.**

**RF board's PLL is programmed.**

**Lens is printed.**

**FPGA programming started.**

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### **Pending Issues**

**RF board is not tested.**

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### **Plans for Upcoming Reporting Period**

**RF board is tested.**

**ADC board is assembled.**

**ADC board is tested.**

**SPI programming between FPGA and RF board is done.**

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